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APPLICANTS Masayuki Furumiya, Kanagawa, JAPAN; Ryota Yamamoto, Kanagawa, JAPAN;				
** CONTINUING DATA ***** <i>no / TT</i>				
** FOREIGN APPLICATIONS ***** <i>yes / TT</i> JAPAN 2002-183471 06/24/2002				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/11/2003				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 6
ADDRESS Darryl G. Walker WALKER & SAKO, LLP Suite 235 300 South First Street San Jose , CA 95113				
TITLE Semiconductor integrated circuit and method of manufacturing the same				
FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)	